

Copyright is held by the author/owner(s). GECCO'08, July 12–16, 2008, Atlanta, Georgia, USA. ACM 978-1-60558-131-6/08/07.

Genetic Programming

The automatic evolution of computer programs

- Tree-based, Koza 1992
- Stack-based, Perkis 1994, Spector 1996 onwards (push-pop GP)

2

4

- Linear GP, Nordin and Banzhaf 1996
- Cartesian GP, Miller 1997
- Parallel Distributed GP, Poli 1996
- Grammatical Evolution, Ryan 1998
- Lots of others...

Cartesian Genetic Programming (CGP)

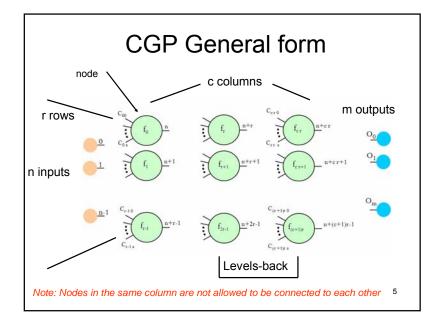
- Grew out of work in the evolution of digital circuits, Miller and Thomson 1997. First mention of the term Cartesian Genetic Programming appeared at GECCO in 1999.
- Originally, represents programs or circuits as a two dimensional grid of program primitives.
- This is loosely inspired by the architecture of digital circuits called FPGAs (field programmable gate arrays)
- The genotype is a list of integers that represent the program primitives and how they are connected together
 - CGP represents programs as graphs in which there are non-coding genes

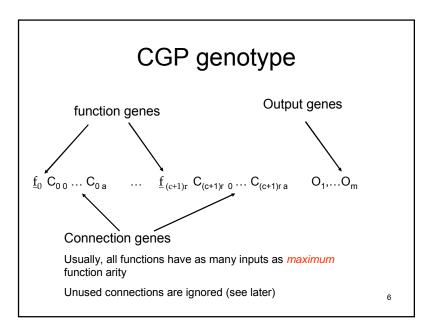
3

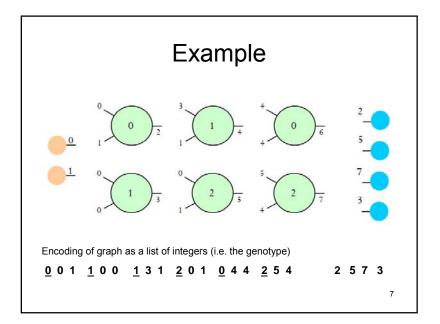
1

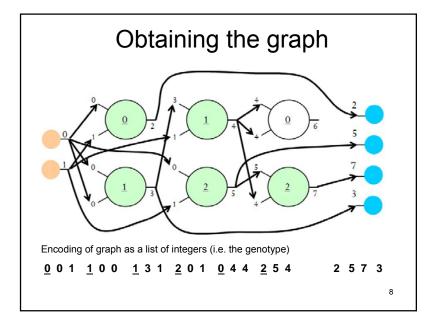
Types of CGP

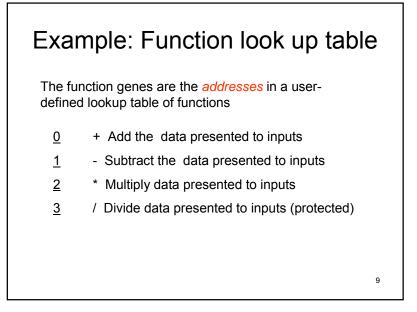
- Classic
- Modular
- Self-modifying
- Developmental
- Cyclic

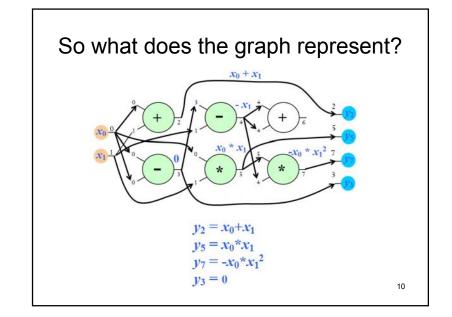


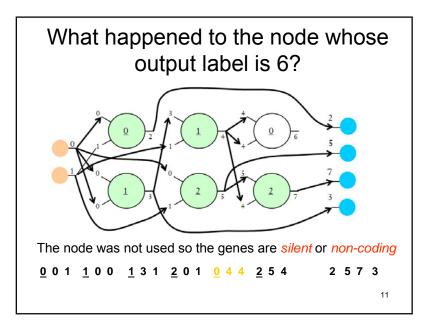


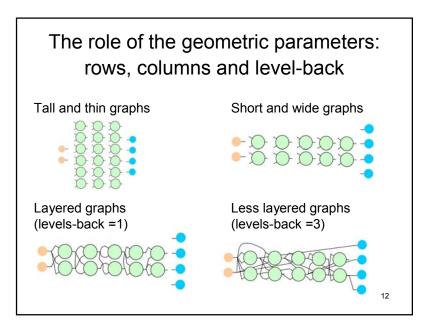






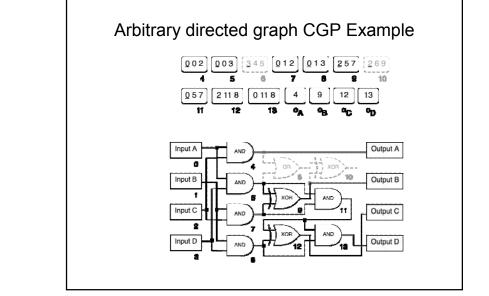


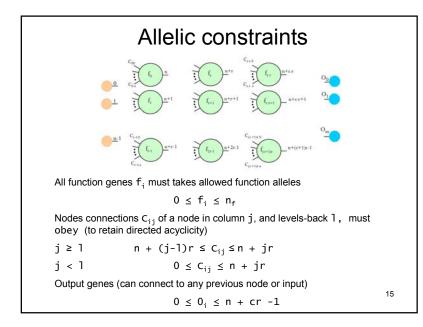


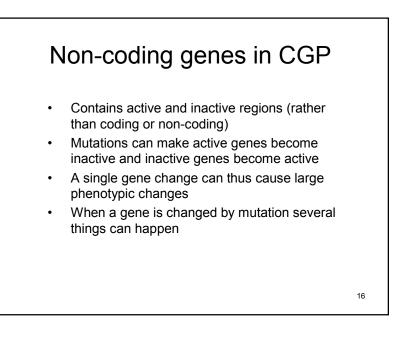


Types of graphs easily controlled

- Depending on *rows*, *columns* and *levels-back* a wide range of graphs can be generated
- When rows =1 and levels-back = columns arbitrary directed graphs can be created with a maximum depth
 - In general choosing these parameters imposes the least constraints. So without specialist knowledge this is the best and most general choice





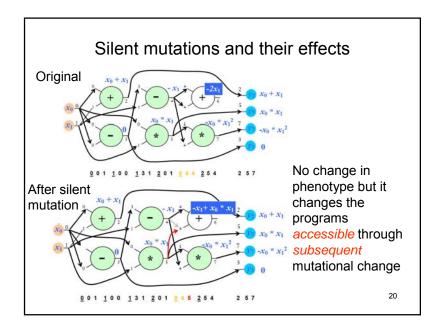


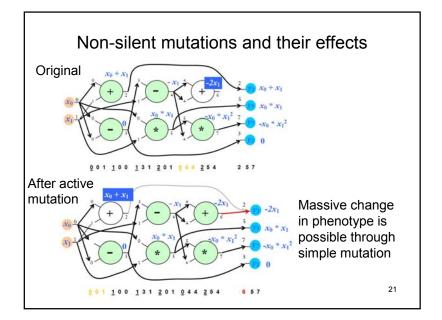
Point mutation	Crossover or not?
 Most CGP implementations only use mutation. Carrying out mutation is very simple. It consists of the following steps. The genes must be chosen to be valid alleles (as in slide 14) Decide how many genes to change:num_mutations <pre>while (mutation_counter < num_mutations)</pre>	 Recombination doesn't seem to add anything (Miller 1999, "An empirical study") However if there are multiple chromosomes with independent fitness assessment then it helps a LOT – see later (Walker, Miller Cavill 2006) Recent work using a floating point representation of CGP has suggested that crossover might be useful (Clegg, Walker, Miller 2007)
17	1

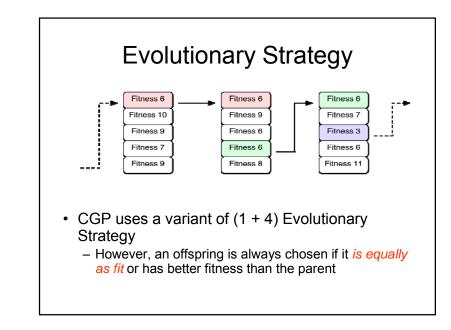
Program changes caused by mutations				
Gene	Gene	Genotypic	Phenotypic	Fitness
was	is	change	change	change
silent	silent	Yes	No	No
active	silent	Yes	Yes	Likely
silent	active	Yes	Yes	Likely
active	active	Yes	Yes	Likely

When genetic changes occur without any fitness change it is often referred to a *neutral* change.

The very interesting aspect is that in CGP most neutral change occurs externally to the phenotype, so it does not have to be processed in any fitness calculation (unlike many other forms of GP) 19

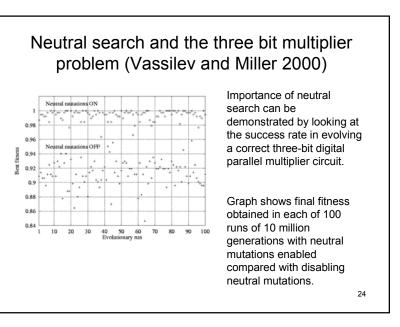




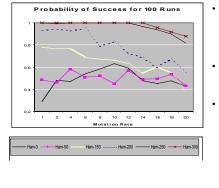


Neutral search is fundamental to success of CGP

 A number of studies have been carried out to indicate the importance to neutral search (Miller and Thomson 2000, Vassilev and Miller 2000, Yu and Miller 2001, Miller and Smith 2006)

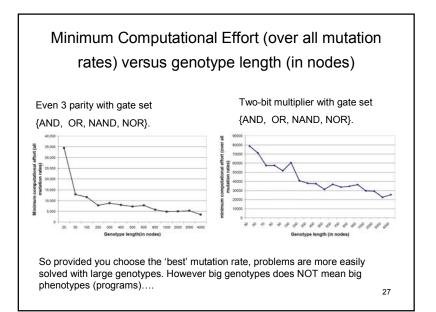


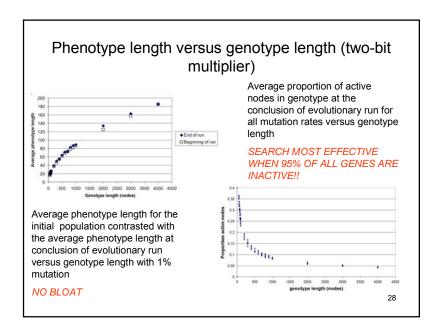
Effectiveness of Neutral Search as a function of mutation rate and Hamming bound (Yu and Miller 2001)



- Hamming Distance H(g,h) g1=213 012 130 432 159 g2=202 033 132 502 652 hamming distance H(g1,g2)=9.
- If genotypes are selected so that $H(g_{new},g_{old}) = 0$. No neutral drift is permitted.
- If genotypes are selected so that $H(g_{new},g_{old}) = length(g)$. Any amount of neutral drift is permitted.

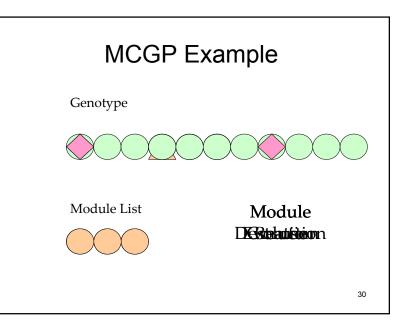
<section-header><figure><figure><figure>

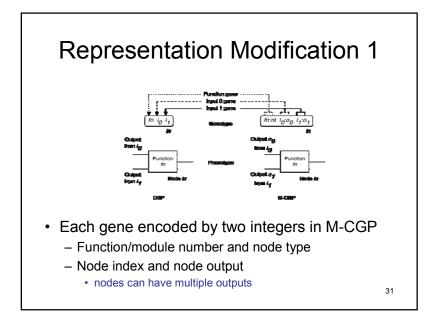


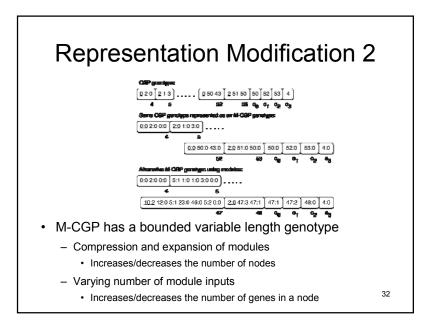


Modular/Embedded CGP (Walker, Miller 2004)

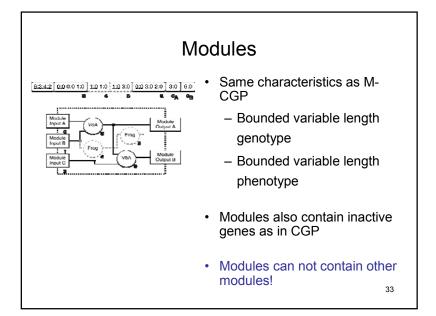
- So far have described a form of CGP (classic) that does not have an equivalent of Automatically Defined Functions (ADFs)
- Modular CGP allows the use of modules (ADFs)
 - Modules are dynamically created and destroyed
 - Modules can be evolved
 - Modules can be re-used

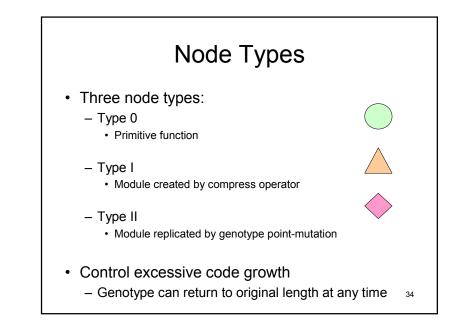


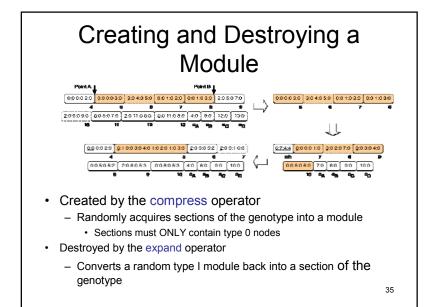


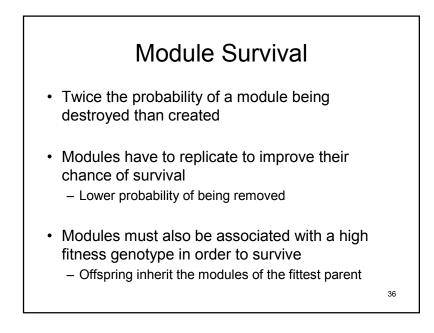


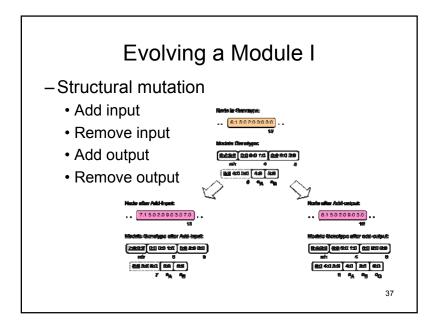
GECCO 2008 Tutorial / Cartesian Genetic Programming

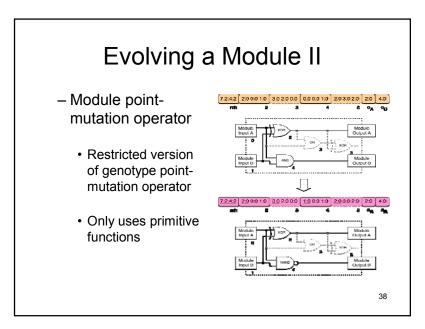


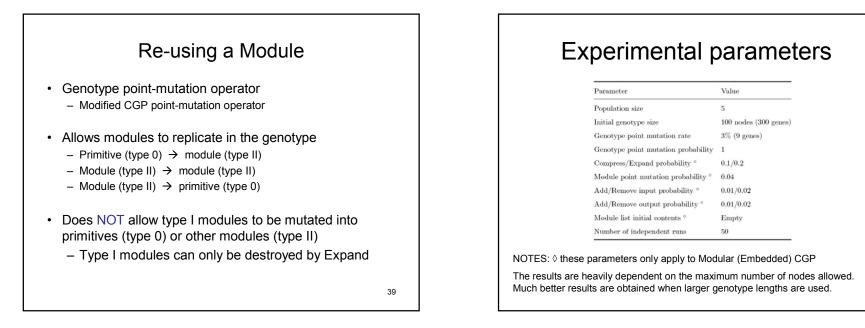


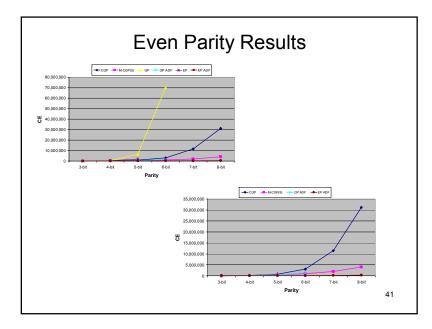


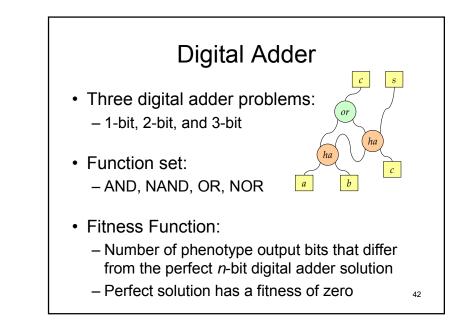


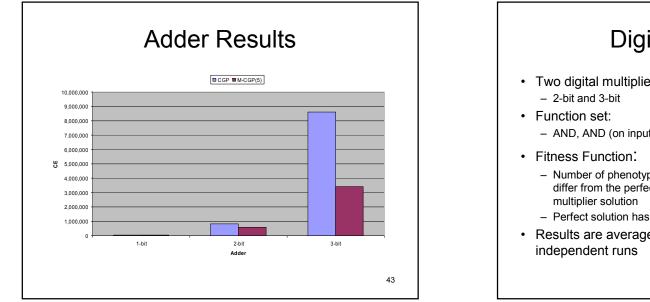


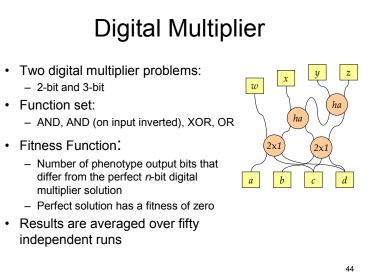


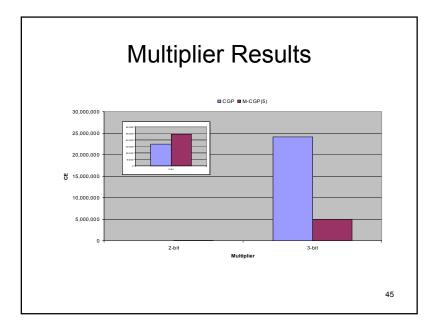


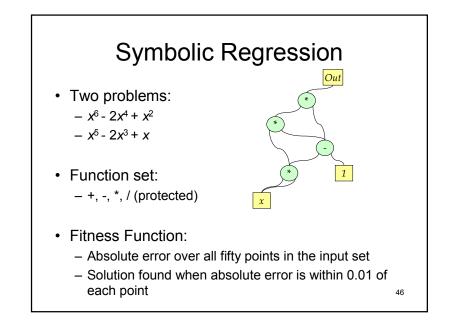


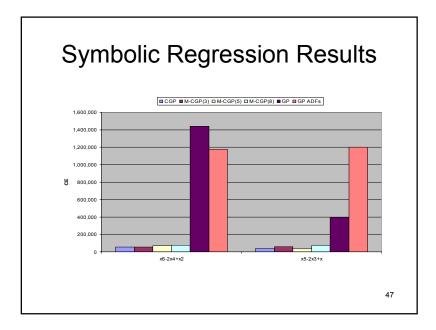


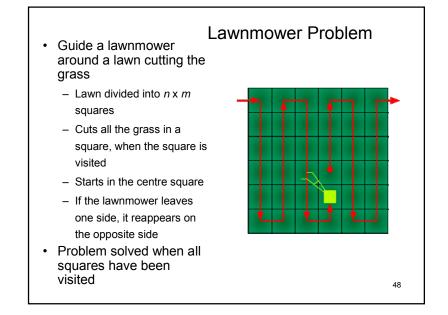


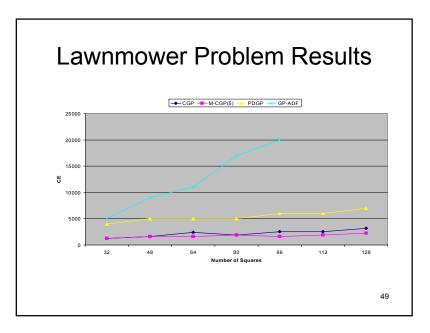


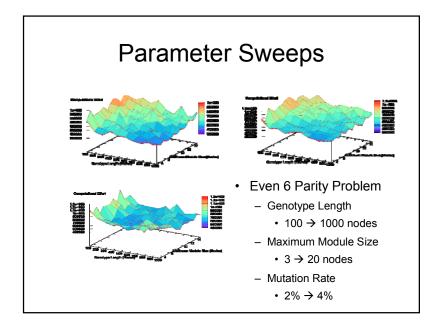






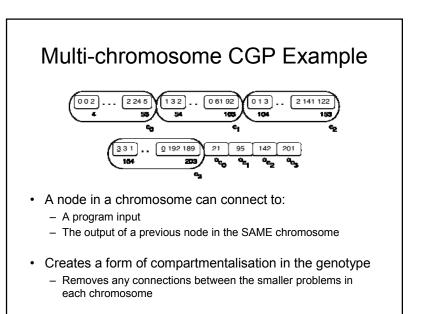


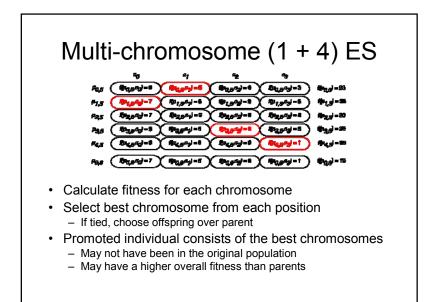




Multi-chromosome Approach

- A multi-chromosome genotype is divided up into *n* equal length sections called "chromosomes"
 - Each chromosome contains an equal number of nodes
- The no. of chromosomes (*n*) is dictated by the no. of outputs of the given problem
 - Each chromosome has a single output
- The entire problem is still represented in a single genotype





Multi-chromosome experiments and Parameters

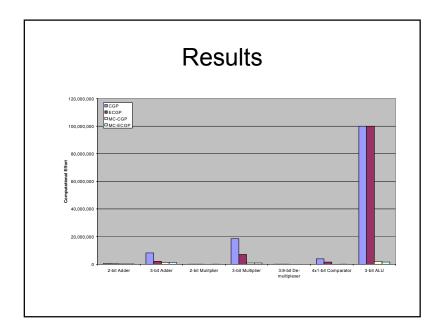
- Adder⁺
 - 2-bit (3 chromosomes)
- 3-bit (5 chromosomes)
 Multiplier *
- 2-bit (4 chromosomes)
- 3-bit (6 chromosomes)
- De-multiplexer [†]
 - 3:8-bit (8 chromosomes)
- Comparator †
 - 4 x 1-bit (18
 - chromosomes)
- Arithmetic Logic Unit *
 - 3-bit (17 chromosomes)

contained 100 nodes (300 genes)

Each chromosome

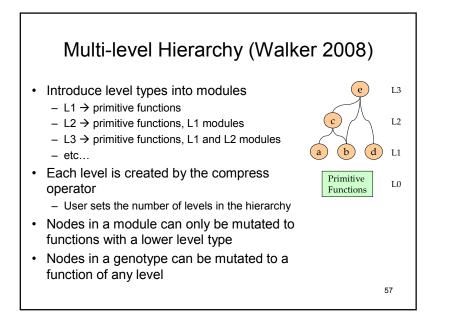
- Function set 1 (*)
 - AND, AND (one input inverted), XOR, OR
- Function set 2 ([†])
 - AND, NAND, OR, NOR

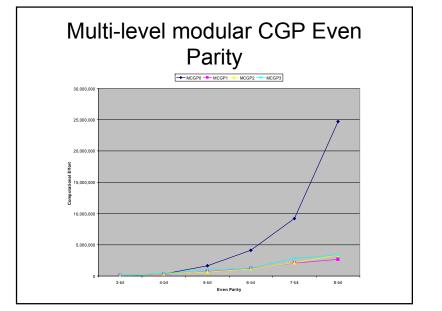
56

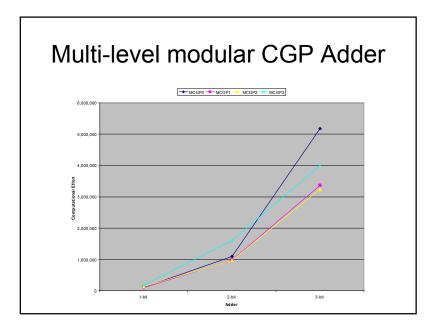


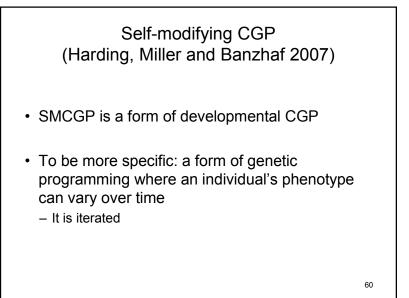
Modules within Modules?

- Currently only allow primitive functions in modules
 Single level hierarchy
- Allow modules within modules
 - Multi-level hierarchy
 - Produce larger building blocks
 - Improve performance
 - Evolve solutions to larger, more complex problems
- ADFs occur inside ADFs in GP, why not have modules inside modules?









Representational differences

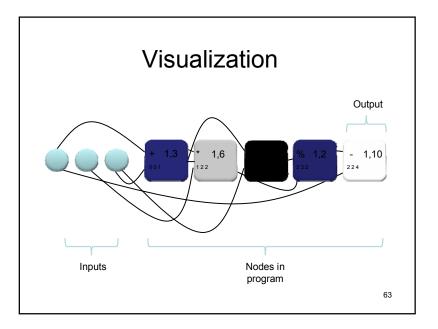
- In CGP nodes connect explicitly
 - -i.e This node connects to node 12.
- In SMCGP nodes have a relative address

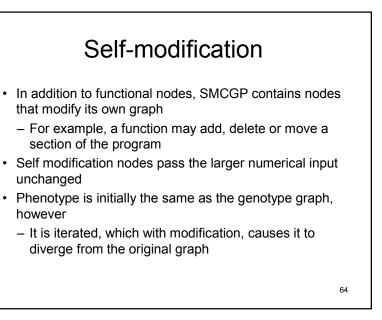
 i.e. This node connects to one 4 nodes back.
 Useful for moving pieces of cgp code around
- CGP node :
 - function & connections
- SMCGP node :
 - function, connections & 3 parameters.

61

Other representational differences

- Input/Outputs handled differently.
 In SMCGP typically the last N-nodes in the graph are used as output nodes
- If a node addresses a node of a negative index, then this is mapped to an input (using modulo arithmetic)





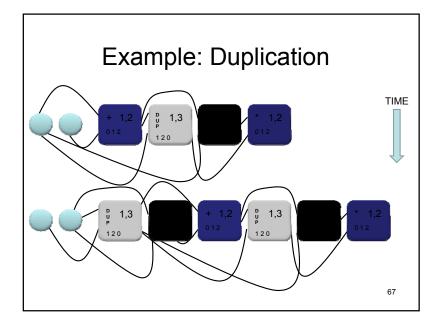
Self-modification process

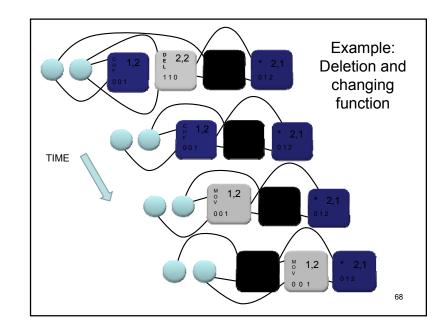
- 1. Evaluate CGP graph
 - Get computational output
- 2. If a node is a modification node and it is activated, add to 'ToDo' list
 - Activated means: If the first input is greater or equal to value to the second input
- 3. When finished evaluating entire graph, parse 'ToDo' list.
- 4. Perform each operation to build modified graph for next iteration

65

Some SMCGP operators

Operator	Parameters	Function
MOVE	Start, End, Insert	Moves each of the nodes between Start and End into the position specified by Insert
DUPE	Start, End, Insert	Inserts copies of the nodes between Start and End into the position specified by Insert
DELETE	Start, End	Deletes the nodes between Start and End indexes
ADD	Insert, Count	Adds Count number of NOP nodes at position Insert
CHF	Node, New Function	Changes the function of a specified node to the specified function
CHC	Node, Connection1, Connection2	Changes the connections in the specified node
CHP	Node, Parameter, New Value	Changes the specified parameter and a given node
FLR		Clears any entries in the pending modifications list
OVR	Start, End, Insert	Moves each of the nodes between Start and End into the position specified by Insert, overwriting existing nodes
DU2	Start, End, Insert	Similar to DUPE, but connections are considered to absolute, rather than relative
		66





Modules ir	n SMCGP
------------	---------

Operator	Parameters	Function
PRC	Start, End	Executes the nodes specified as a procedure.

- A special function can call another part of the graph as a procedure
- This section of graph could be made up of active nodes, or nodes neutral to the main graph
- Procedures can call other procedures.
- Procedures can self modify
- Inputs to this procedure are the inputs to the calling node

71

Example: Generating Sequences

- We limit the functional nodes to + and -
- The task is on each iteration (0,1,2,...) to produce the next number in a sequence
 - Here, we ask for the squares: 1,4,9,16,25 etc.
- · The only input was the iteration, i
- Fitness calculated by iterating from 0 to 9 and counting the longest sequence from zero that were correct
- Without self-modification this task is impossible

70

Input, <i>i</i>	Evolved program	Output	
0	0 + <i>i</i>	0	
1	0 + <i>i</i>	1	
2	0 + <i>i</i> + <i>i</i>	4	
3	0 + <i>i</i> + <i>i</i> + <i>i</i>	9	
4	0 + <i>i</i> + <i>i</i> + <i>i</i> + <i>i</i>	16	

Evolving Digital Circuits

- In this experiment we tackled the well known problem of evolving circuits for solving even-parity
- We used a restricted function set, that is well studied in the literature
 - AND OR NAND NOR
- This set of functions make the problem *very hard* to solve

Evolving Parity Circuits					
Number of inputs	4	5	6	7	8
SMCGP	28811	58194	191493	352901	583712
Speedup compared with MCGP	2.27	3.13	1.44	0.76	0.5
Speedup over CGP	2.84	5.04	4.88	8.53	10.13

Evolving big parity functions

- The largest parity problem solved to date with a direct GP approach appears to be 22 inputs
 - Although general solutions have been found.
 - The function set used includes many more bitwise operations – including XOR
- We attempted to produce a solution that could be iterated to find *any* size circuit

74

Evolving big parity functions

- The challenge:
 - Evolve an SMCGP program that solves 2input even parity. After iterating the growth algorithm once, it should solve 3-input. After a second time, 4-input and so on
- We were able to evolve (and test) to 24 bit input.
 We think it is a general solution but haven't verified this yet

75

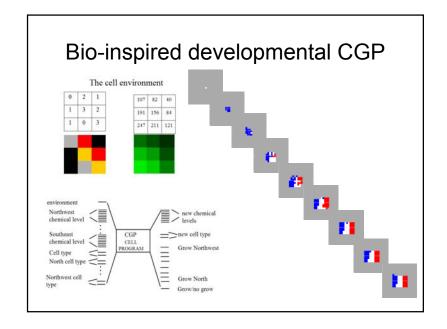
2719

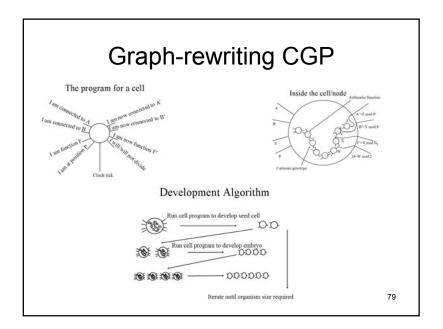
SMCGP Conclusions

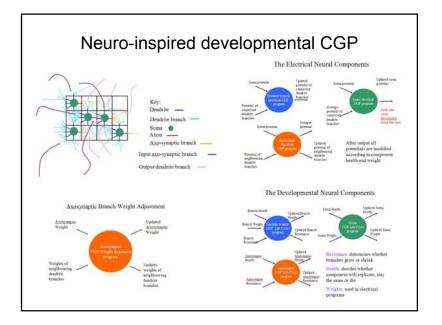
- Discussed a promising new variant of CGP that bridges the divide between artificial developmental systems and genetic programming
- This is done by directly producing a phenotype capable of performing a computation
- We have shown we can solve problems that cannot be solved by a conventional GP system.
- In other experiments we have shown that performance appears to be similar on problems where there is no inherent advantage for the self-modification

Developmental CGP

- Various types of CGP inspired by biological development, graph re-writing and neuro-develop have been devised
 - Biological developmental (Miller 2003, 2004)
 - Graph re-writing (Miller 2003)
 - Neuro-developmental (Khan, Miller and Halliday 2007, 2008)





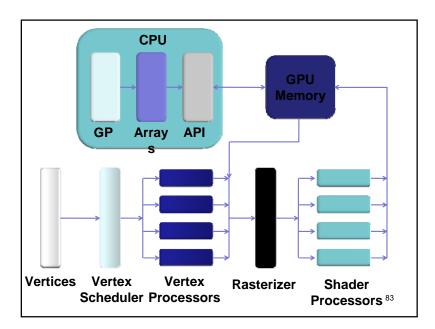


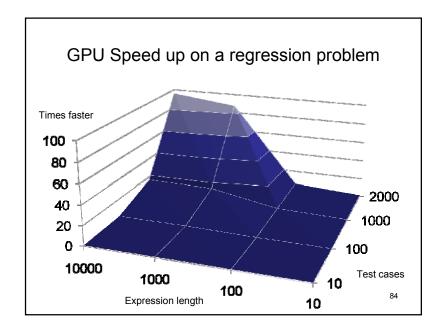
Cyclic CGP

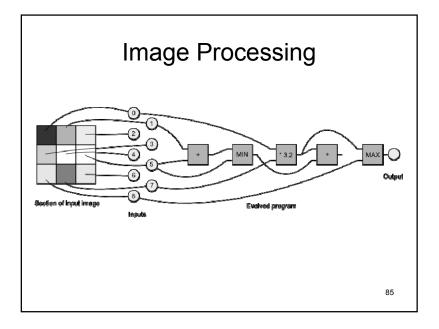
- When outputs are allowed to connect to inputs through a clocked delay (flip-flop) it is possible to allow CGP to include feedback.
- By feeding back outputs generated by CGP to an input, it is possible to get CGP to generate sequences

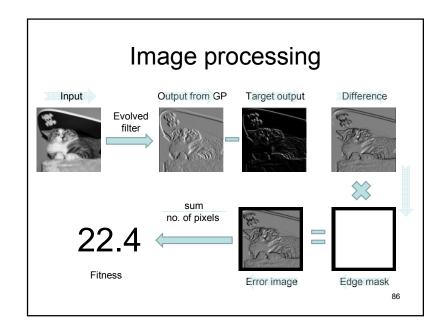
GPU Implementation (Harding and Banzhaf 2007)

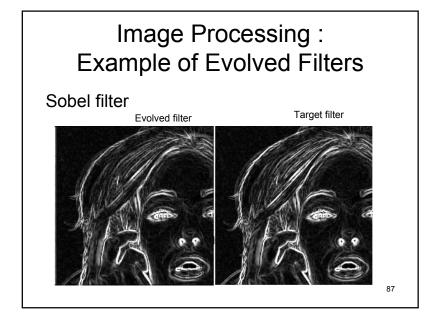
- A guaranteed maximum program length makes it easy to use CGP on more limited platforms.
- We have developed a version of CGP that runs on Graphics Processing Units
 - Limited program length
 - Memory constraints
 - But fast, parallel architecture

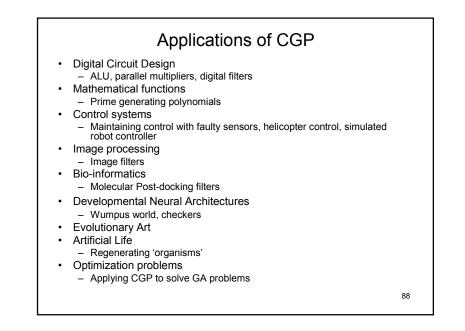












CGP Web Resources

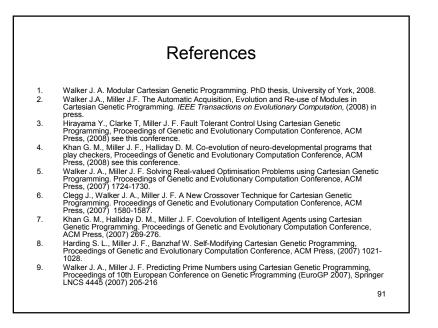
· Home site:

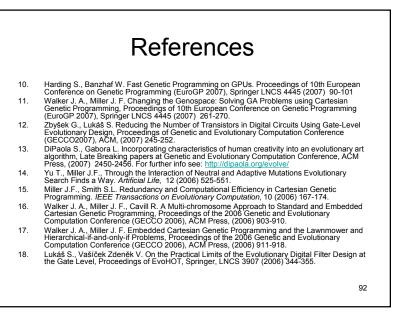
http://www.cartesiangp.co.uk

- Julian Miller: <u>http://www.elec.york.ac.uk/intsys/users/jfm7/</u>
- Simon Harding: <u>http://www.evolutioninmaterio.com/</u> <u>http://www.gpgpgpu.com</u>

Conclusions

- Cartesian Genetic Programming is a graph based GP method
- Genetic encoding is compact, simple and easy to implement and can handle multiple outputs easily.
- The unique form of genetic redundancy in CGP makes mutational search highly effective
- The effectiveness of CGP has been compared with many other GP methods and it is very competitive
- The CGP method is still being developed (i.e. modular CGP, self-modifying CGP, neuro-developmental CGP)
- A method has been developed for CGP to output lists of numbers so that it can be applied to any problem that genetic algorithms can be applied to (see Walker and Miller 2007)





References

- Walker J. A., Miller J. F. Improving the Evolvability of Digital Multipliers Using Embedded Cartesian Genetic Programming and Product Reduction. Proceedings of 6th International Conference in Evolvable Systems (ICES 2005), Springer, LNCS 3637 (2005) 131-142.
- Liu H., Miller J. F., Tyrrell Á. M., Intrinsic evolvable hardware implementation of a robust biological development model for digital systems, Proceedings of the NASA/DOD Evolvable Hardware Conference, IEEE Computer Society (2005) 87-92.
- Walker J. A., Miller J. F. Investigating the performance of module acquisition in Cartesian Genetic Programming, Proceedings of the 2006 conference on Genetic and Evolutionary Computation (GECCO 2005), ACM Press (2005) 149-1656.
- Harding S. L., Miller J. F. Evolution of Robot Controller Using Cartesian Proceedings of the 6th European Conference on Genetic Programming (EuroGP 2005) Springer LNCS 3447 (2005) 62-72.
- Liu H., Miller J. F., Tyrrell A. M. A Biological Development Model for the Design of Robust Multiplier. Applications of Evolutionary Computing: EvoHot 2005, Springer LNCS 3449 (2005) 195-204
- DiPaolo S. Evolving Creative Portrait Painter Programs using Darwinian Techniques with an Automatic Fitness Function. Electronic Visualizationa and the Arts Conference (2005)
- Miller J. F., Thomson P. Beyond the Complexity Celling: Evolution, Emergence and Regeneration. Workshop on Regeneration and Learning in Developmental Systems, Genetic and Evolutionary Computation Conference (2004).
- Liu H., Miller J. F., Tyrrell A. M. An Intrinsic Robust Transient Fault-Tolerant Developmental Model for Digital Systems. Workshop on Regeneration and Learning in Developmental Systems, Genetic and Evolutionary Computation Conference (2004).
- Zhang Y., Smith S. L., Tyrrell A. M. Digital circuit design using intrinsic evolvable hardware, Proceedings of the NASA/DOD Evolvable Hardware Conference, IEEE Computer Society (2004) 55-62.

References

- Miller J. F. Evolving a self-repairing, self-regulating, French flag organism. Proceedings of Genetic and Evolutionary Computation Conference (GECCO 2004), Springer LNCS 3102 (2004) 129-139.
- Walker J. A., Miller J. F. Evolution and Acquisition of Modules in Cartesian Genetic Programming. Genetic Programming 7th European Conference, EuroGP 2004, Proceedings. Springer LNCS 3003 (2004) 187-197.
- Garmendia-Doval B., Miller J.F., Morley S.D. Post Docking Filtering using Cartesian Genetic Programming. *Genetic Programming Theory and Practice II*. O'Reilly U-M., Yu T., Riolo R., Worzel B. (Eds.). University of Michigan Illinois USA. Springer (2004).
- Rothermich J., Wang F., Miller J. F. Adaptivity in Cell Based Optimization for Information Ecosystems. Proceedings of the 2003 Congress on Evolutionary Computation (CEC03) IEEE Press (2003) 490-497.
- Miller J. F. Evolving developmental programs for adaptation, morphogenesis, and self-repair. Proceedings of the 7th European Conference on Artificial Life, Springer LNAI 2801 (2003) 256-265.
- Miller J. F., Thomson P. A Developmental Method for Growing Graphs and Circuits. Proceedings of the 5th International Conference on Evolvable Systems: From Biology to Hardware, Springer LNCS 2606 (2003) 93-104.
- Miller J.F., Banzhaf W., Evolving the Program for a Cell From French Flags to Boolean Circuits. Kumar S., Bentley P. On Growth, Form and Computers. Elsevier Academic Press (2003).
- Lukáš S. Evolvable Components From Theory to Hardware Implementations, Berlin, Springer, 2003, ISBN 3-540-40377-9

94

References 36. Voss, Mark S. (2003). Social programming using functional swarm optimization. In Proceedings of IEEE Swarm Intelligence Symposium (SIS03). Voss, Mark S. and James C. Howland, III (2003). Financial modelling using social 37. programming. In FEA 2003: Financial Engineering and Applications, Banff, Alberta. Rothermich J., Miller J. F. Studying the Emergence of Multicellularity with Cartesian Genetic 38 Programming in Artificial Life. Proceedings of the 2002 U.K. Workshop on Computational Intelligence (2002). Yu T., Miller J. F. Finding Needles in Haystacks Is Not Hard with Neutrality. Proceedings of the 39 5th European Conference on Genetic Programming (EuroGP2002), Springer LNCS 2278 (2002) 13-25 Lukáš S. Image Filter Design with Evolvable Hardware, Proceedings of Evolutionary Image Analysis and Signal Processing (EvolASP2002), Springer LNCS 2279 (2002) 255-266. 40. Yu T., Miller J. F. Neutrality and Evolvability of a Boolean Function Landscape, Proceedings of the 4th European Conference on Genetic Programming (EuroGP2001). Springer LNCS, 2038, (2001) 204-217. 41. 42 Miller J. F., Hartmann M. Evolving messy gates for fault tolerance: some preliminary findings. Proceedings of the 3rd NASA/DOD Workshop on Evolvable Hardware (EH'01). IEEE Computer Society (2001) 116-123. Miller J. F., Hartmann M. Untidy evolution: Evolving messy gates for fault tolerance", Proceedings of the 4th International Conference on Evolvable Systems; From Biology to 43 Hardware. Springer LNCS 2210 (2001) 14-25. 95

References 44. Miller J. F. What bloat? Cartesian Genetic Programming on Boolean problems. Genetic and Evolutionary Computation Conference, Late breaking paper (2001) 295 - 302. Miller J.F., Kalganova T., Lipnitskaya N., Job D. The Genetic Algorithm as a Discovery Engine: Strange Circuits and New Principles. Creative Evolutionary Systems. Morgan Kaufmann 45. (2001) Miller J.F., Job D., Vassilev V.K. Principles in the Evolutionary Design of Digital Circuits - Part I. Journal of Genetic Programming and Evolvable Machines, 1 (2000) 8-35. 46. 47 Miller J.F., Job D., Vassilev V.K. Principles in the Evolutionary Design of Digital Circuits - Part II. Journal of Genetic Programming and Evolvable Machines, 3 (2000) 259-288. Vassilev V. K., Miller J. F. Towards the Automatic Design of More Efficient Digital Circuits. 48 Proceedings of the 2nd NASA/DOD Workshop on Evolvable Hardware. IEEE Computer Society (2000) 151-160. Vassilev V. K., Miller J. F. Scalability Problems of Digital Circuit Evolution. Proceedings of the 2nd NASA/DOD Workshop on Evolvable Hardware. IEEE Computer Society (2000) 55-64. 49. Miller J. F., Thomson P. Cartesian Genetic Programming. Proceedings of the 3rd European Conference on Genetic Programming. Springer LNCS 1802 (2000) 121-132. 50. Vassilev V. K., Miller J. F. The Advantages of Landscape Neutrality in Digital Circuit Evolution. 51 Proceedings of the 3rd International Conference on Evolvable Systems: From Biology to Hardware. Springer LNCS 1801 (2000) 252-263. 96

References

- Ashmore, L. An investigation into cartesian genetic programming within the field of evolutionary art. http://www.emoware.org/evolutionary_art.asp, Department of Computer 52. Science, University of Birmingham (2000)
- Miller J. F. Evolution of Digital Filters using a Gate Array Model. Proceedings of the First EvolASP'99 Workshop on Image Analysis and Signal Processing. Springer LNCS 1596 (1999) 53 17-30.
- Miller J. F. Digital Filter Design at Gate-level using Evolutionary Algorithms. Proceedings of the 1st Genetic and Evolutionary Computation Conference (GECCO'99). Morgan Kaufmann (1999) 1127-1134. 54.
- 55. Miller J. F. An empirical study of the efficiency of learning boolean functions using a Cartesian Genetic Programming Approach. Proceedings of the 1st Genetic and Evolutionary Computation Conference (GECCO'99). Morgan Kaufmann (1999) 1135-1142.
- Miller J. F. On the filtering properties of evolved gate arrays. Proceedings of the First NASA/DOD Workshop on Evolvable Hardware (EH'99). IEEE Computer Society (1999) 2-11. 56.
- Vasilev V. K., Miller J. F., Fogarty T. C. On the Nature of Two-Bit Multiplier Landscapes. Proceedings of the First NASADOD Workshop on Evolvable Hardware (EH'99). IEEE Computer Society (1999) 36-45. 57.
- Computer Society (1999) 36-49.
 Miller J. F., Kalganova T., Lipnitskaya N., Job D. The Genetic Algorithm as a Discovery Engine: Strange Circuits and New Principles. Proceedings of the workshop on the AISB Symposium on Creative Evolutionary Systems (CES'99) (1999) 65-74.
 Vassilev V. K., Miller J. F., Fogarty T. C. Digital Circuit Evolution and Fitness Landscapes. Proceedings of the Congress on Evolutionary Computation. IEEE Press (1999) 1299-1306. 58.
- 59.

97

References

- Kalganova T., Miller J. F., Evolving More Efficient Digital Circuits by Allowing Circuit Layout Evolution and Multi-Objective Fitness. Proceedings of the First NASA/DOD Workshop on Evolvable Hardware (EH'99). IEEE Computer Society (1999) 54-63. 60.
- Miller J. F., Thomson P. Evolving Digital Electronic Circuits for Real-Valued Function 61 Generation using a Genetic Algorithm . Proceedings of the Third Annual Conference on Genetic Programming. Morgan Kaufmann (1998) 863-868.
- Miller J. F., Thomson P. Aspects of Digital Evolution: Evolvability and Architecture. Proceedings of The Fifth International Conference on Parallel Problem Solving from Nature (PPSNV). Springer LNCS 1498 (1998) 927-936. 62
- Willer J. F., Thomson P. Aspects of Digital Evolution: Geometry and Learning. Proceedings of the 2nd International Conference on Evolvable Systems: From Biology to Hardware. Springer LNCS 1478 (1998) 25-35. 63.
- 64.
- LNCG 1476 (1990) 25-35.
 Kalganova T, Miller J. F, Fogarty T. C. Some Aspects of an Evolvable Hardware Approach for Multiple-Valued Combinational Circuit Design Proceedings of the 2nd International Conference on Evolvable Systems: From Biology to Hardware. Springer LNCS 1478 (1998) 78-89.
 Miller J.F., Thomson P., Fogarty T.C. Designing Electronic Circuits Using Evolutionary Algorithms: Arithmetic Circuits: A Case Study. Genetic Algorithms and Evolution Strategies in Engineering and Computer Science: Recent Advancements and Industrial Applications. Quagliarella, D., Periaux J., Poloni C., Winter G. (Eds.). Wiley (1997) 65.